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Depositor: Karen Cing-Mars

(Signature & date)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of _____:
Joseph O. Marsh, et al. :

April 02, 2004
Group Art Unit: Not yet assigned

Serial No. :10/708,788

Examiner: Not yet assigned

Filed: 3/25/04 :

International Business Machines Corporation
2070 Route 52
Hopewell Junction, NY 12533

TITLE: SYSTEM AND METHOD OF DIGITALLY TESTING AN ANALOG DRIVER CIRCUIT

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to the duty of disclosure set forth in 37 C.F.R. 1.56, and further pursuant to the provisions of 37 C.F.R. 1.97 and 1.98, applicants hereby respectfully submit copies of the non-US patents and publications as listed on Form PTO-1449, attached hereto.

In citing these documents, no representation is made nor intended as to the pertinency or non-pertinency of the art, that better art than that listed is not available, or that other art is not applicable.

No fee is believed to be due for this submission. If any fees are required, however, the Commissioner is hereby authorized to charge such fees to Deposit Account No. 09-0458.

Respectfully submitted,
Joseph O. Marsh, et al

By

Mark R. Bilak

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FIS920030349US1

APR 06 2004

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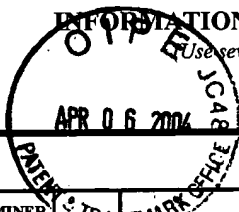
		"DIGITAL SYSTEMS TESTING AND TESTABLE DESIGN", IEEE PRESS, Ch. 9.3 - 9.9, 1990.
		"BIST FOR D/A AND A/D CONVERTERS", IEEE Design & Test of Computers v. 13 n. 4 Winter 1996, pp. 40 - 49

DATE CONSIDERED

Form PTO-A820
(also form PTO-1449)

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)



Docket Number (Optional)

FIS920030349US1

Application Number

10/708,788

Applicant(s)

JOSEPH O. MARSH, ET AL.

Filing Date

3/25/04

Group Art Unit

NOT ASSIGNED

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		Serial No. 10/604,025, "DATA TRANSCEIVER AND METHOD FOR EQUALIZING THE DATA EYE OF A DIFFERENTIAL INPUT DATA SIGNAL ", Camara, et al.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.